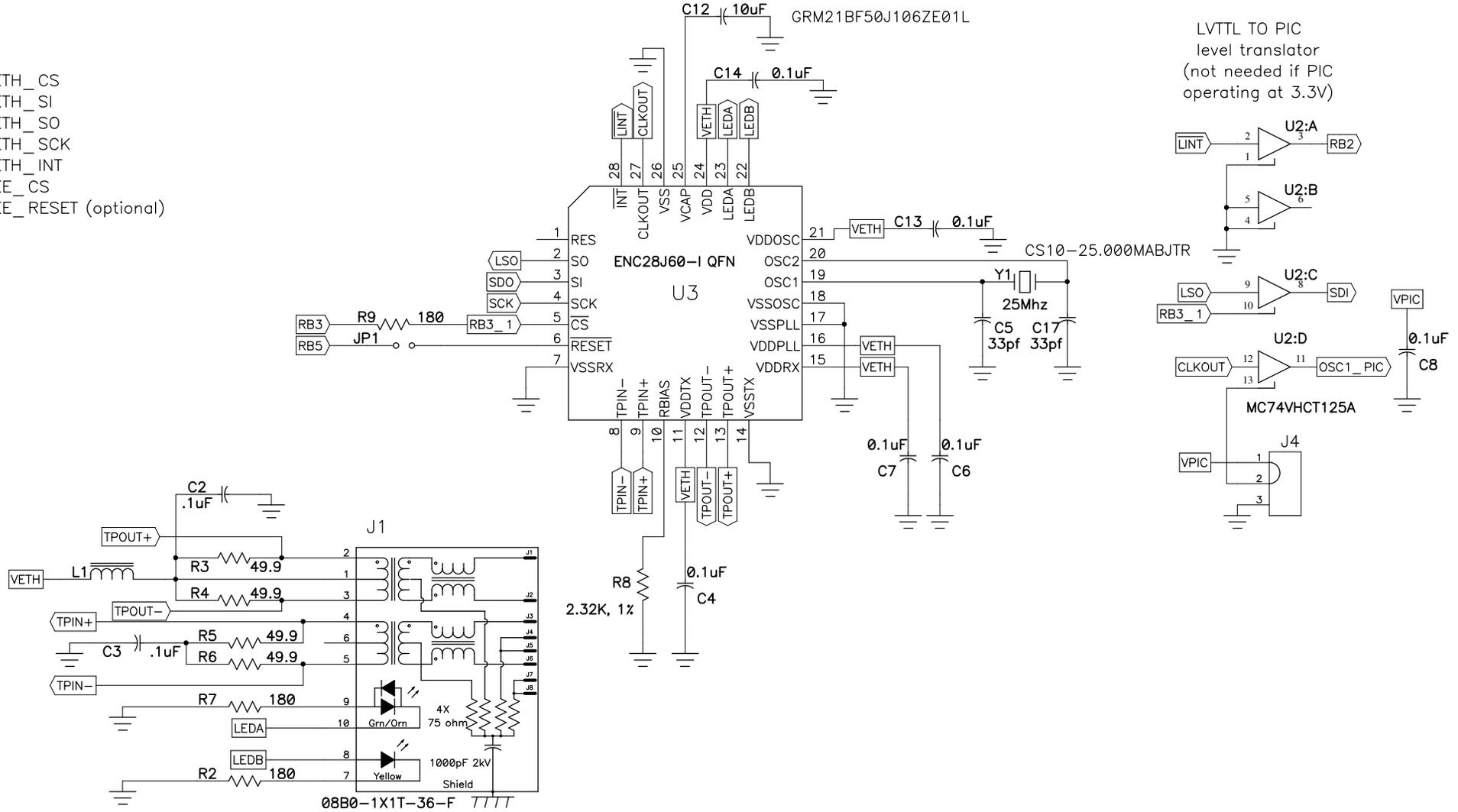
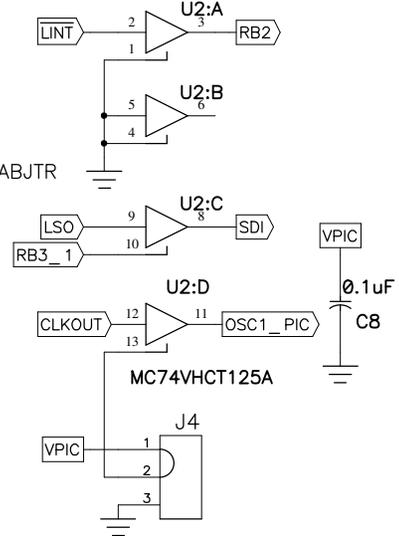


RB3 - ETH_CS
 RC5 - ETH_SI
 RC4 - ETH_SO
 RC3 - ETH_SCK
 RB2 - ETH_INT
 RB4 - EE_CS
 RB5 - EE_RESET (optional)



LVTTTL TO PIC
 level translator
 (not needed if PIC
 operating at 3.3V)



Title Ethernet PICtail™ Daughter Board		
Size B	MICROCHIP 03-01841	Rev 5
Date: Fri Apr 24, 2009	Eng: R. Rojvanit	Drawn by: S. Humberd
Filename: 03-01841_Rev5.sch	Sheet 1 of 2	

